

WHAT IS CLAIMED IS:

1. A method of testing comprising:

5 transmitting a first data via a first transmitter;
transmitting a second data via said first transmitter, wherein said second data
comprises a first portion of an identifier which corresponds to said first
transmitter;
receiving the first data and said second data at a first receiver;
10 transmitting a first feedback data from said first receiver to said first transmitter,
in response to determining said first data is correct;
transmitting a second feedback data from said first receiver to said first
transmitter, wherein said second feedback data is not equal to said first
feedback data, in response to determining said first data is not correct.

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2. The method of claim 1, wherein said first data comprises a test pattern, said
identifier comprises a sequence of bits, and wherein said first portion comprises a single
bit of said sequence of bits.

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3. The method of claim 1, wherein said first receiver determines said first data is
correct in response to determining said received first data matches an expected data.

4. The method of claim 1, wherein said first feedback data is equal to said second
data.

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5. The method of claim 4, wherein said second feedback data comprises the
complement of said second data.

6. The method of claim 1, further comprising assigning said identifier to said first transmitter.

7. The method of claim 6, further comprising transmitting a third data from a second transmitter, wherein said second transmitter is adjacent to said first transmitter, and wherein said third data does not equal said second data.

8. The method of claim 1, wherein said transmitter is configured to transmit said first and second data at a first speed, and wherein said receiver is configured to convey said first and second feedback data at a second speed, wherein said second speed is lower than said first speed.

9. A system comprising:

15 a first transmitter, wherein said first transmitter is configured to:
transmit a first data; and
transmit a second data, wherein said second data comprises a first portion
of an identifier which corresponds to said first transmitter;

20 a first receiver coupled to said first transmitter, wherein said first receiver is
configured to:
receive the first data and second data;
determine whether said received first data is correct;
transmit a first feedback data to said first transmitter, in response to
25 determining said first data is correct; and
transmit a second feedback data to said first transmitter, wherein said
second feedback data is not equal to said first feedback data, in
response to determining said first data is not correct.

10. The system of claim 9, wherein each of said first data and said identifier comprise a sequence of bits, and wherein said first portion comprises a single bit of said sequence of bits.

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11. The system of claim 9, wherein said first receiver is configured to determine said first data is correct responsive to determining said received first data matches an expected data.

10 12. The system of claim 9, wherein said first feedback data is equal to said second data.

13. The system of claim 12, wherein said second feedback data comprises the complement of said second data.

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14. The system of claim 13, further comprising a second transmitter adjacent to said first transmitter, wherein said second transmitter is configured to transmit a third data, wherein said third data does not equal said first data.

20 15. The system of claim 9, wherein said first transmitter is configured to transmit said first and second data at a first speed, and wherein said first receiver is configured to convey said first and second feedback data at a second speed, wherein said second speed is lower than said first speed.

25 16. A system comprising:

a first transmitter configured to:
transmit a first data; and

transmit a second data, wherein said second data comprises a first portion of an identifier which corresponds to said first transmitter;

a first receiver coupled to said first transmitter, wherein said first receiver is
5 configured to:

receive the first data and second data;

determine whether said received second data is correct;

transmit a first feedback data to said first transmitter, in response to determining
said first data is correct; and

10 transmit a second feedback data to said first transmitter, wherein said second
feedback data is not equal to said first feedback data, in response to
determining said first data is not correct.

17. The system of claim 15, wherein each of said first data and said identifier
15 comprise a sequence of bits, and wherein said first portion comprises a single bit of said
sequence of bits.

18. The system of claim 15, wherein said first receiver is configured to determine said
first data is correct responsive to determining said received first data matches an expected
20 data.

19. The system of claim 15, wherein said first feedback data is equal to said
second data.

25 20. The system of claim 18, wherein said second feedback data comprises the
complement of said second data.

21. The system of claim 19, further comprising a second transmitter adjacent to said first transmitter, wherein said second transmitter is configured to transmit a third data, wherein said third data does not equal said first data.

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